

**ABSTRACT OF THE DISCLOSURE**

An electronic camera in which an analog signal output from a CCD image sensor is AD-converted and digital processing is performed on the AD-converted by an A/D converter in an analog front end, and the AD-converted signal DMA-transferred to a  
5 RAM through line memories in a signal processor. The signal processor controls a basic operating clock signal generated from a clock signal generation circuit so that the frequency of the clock signal is reduced during a read period in which processing including AD conversion of the analog signal output from the CCD image sensor is performed.